SUNDAY

TUTORIAL WORKSHOPS

Formal Hardware Verification: A Users' View Summary	Designing Real-life Applications Using Synopsys Behavioral Compiler	Design of Integrated Systems Including MEMS and ASICs	Deep Sub-Micron Interconnect Modeling and Analysis for Performance and Reliability
	8:00		

12:00 LUNCH BREAK

Core-based Design of Systems on a Chip	Designing Real-life Applications Using Synopsys Behavioral Compiler	Design of Integrated Systems Including MEMS and ASICs	Deep Sub-Micron Interconnect Modeling and Analysis for Performance and Reliability
	1:00		
	5:00		

MONDAY

REGISTRATION
7:30-4:30

9:00
Opening Remarks and
Technical Program Overview

9:30
Keynote Speech
Raul Campasano
"Design Technology for System on a Chip"

12:00
Monday Author's Luncheon

ASIC Architectures	Analog and Mixed Signal	Design for Test
1:30	***Invited Paper***	***Invited Paper***
2:00 MP1.1	MP2.1	MP3.1
2:25 MP1.2	MP2.2	MP3.2
2:50 MP1.3	MP2.3	MP3.3
3:15-3:35 BREAK		
3:35 MP1.4	MP2.4	MP3.4
4:00 MP1.6	MP2.5	
4:25 MP1.7	MP2.6	

5:30-6:30 Social Hour

6:30-9:00 Banquet
James Bell
"NASA Mars Rovers & Orbiters: Imaging the Geologic History of hte Red Planet"

TUESDAY

7:30-8:15 Tuesday Author's Breakfast

Low Power	Novel Devices and Circuits
8:20 TA1.1	TA2.1
8:45 TA1.2	TA2.2
9:10 TA1.3	TA2.3
9:35 TA1.4	TA2.4
10:05-10:35 BREAK	
10:25 TA1.5	TA2.5
10:50 TA1.6	TA2.6
11:15 TA1.7	
11:40 TA1.8	

12:05 Luncheon Speaker
Vic Kulkarni
"Silicon Intellectual Property: The Passport to System-on-a-chip"

ASIC CAD Applications	ASIC Architectures
1:30	***Invited Paper***
2:00 TP1.1	TP2.1
2:25 TP1.2	TP2.2
2:50 TP1.3	TP2.3
3:15-3:35 BREAK	
3:35 TP1.4	TP2.4
4:00 TP1.5	TP2.5
Resusable Architectures and Intellectual Property	
4:25 TP1.6	TP3.1
4:50 TP1.7	TP3.2
5:15 TP1.8	TP3.3
5:40 TP1.9	TP3.4

7:30 Panel Discussion
"Intellectual Property Companies
A Key Industry Megatrend for 2002"

WEDNESDAY

7:30-8:15 Wednesday Author's Breakfast

Simulation and Modeling for High Level Design	Digital Signal Processing and Image Processing
8:20 WA1.1	WA2.1
8:45 WA1.2	WA2.2
9:10 WA1.3	WA2.3
9:35 WA1.4	WA2.4
10:05-10:35 BREAK	
10:25 WA1.5	WA2.5
10:50	WA2.6
11:15	WA2.7
11:40	WA2.8

12:05 LUNCH BREAK

High Performance Circuits	MEMs and Sensors
1:30 WP1.1	WP2.1
1:55 WP1.2	WP2.2
2:20 WP1.3	WP2.3
2:45 WP1.4	WP2.4
3:20	WP2.5